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ABSTRACT OF THE DISCLOSURE

A semiconductor device with which a panel having a large area or a narrowly margined with the circumferrencial space minimized can be manufactured stably with a high yield. The semiconductor device comprises a TFT substrate having a plurality of pixels of a plurality of TFT (thin film transistors) provided on the substrate in which a peripheral wire is arranged along the outer periphery of the TFT substrate and connected to a constant potential.